Request for Judicial Notice Exhibit H

28

4 5

6

7 8

9

10

11

12 13

14 15

16 17

18

19

20

21

22

23 24

25 26

27

28

Plaintiff Micron Technology, Inc. ("Micron") alleges as follows for its Complaint for Declaratory Judgment against defendant MOSAID Technologies Incorporated ("MOSAID"):

PARTIES

- 1. Micron is a Delaware corporation with its principal place of business in Boise, Idaho. Micron is engaged in the business of designing, manufacturing and selling advanced semiconductor products.
- 2. MOSAID is a foreign corporation with its principal place of business in Kanata, Ontario, Canada. Upon information and belief, MOSAID's primary business is licensing and enforcing patents whose claims purportedly cover semiconductor technology.

NATURE OF THE DISPUTE

- 3. In this action, Micron seeks a declaration that: (i) it does not directly or indirectly infringe fourteen United States patents owned by MOSAID; and (ii) these fourteen U.S. patents owned by MOSAID are invalid. These fourteen MOSAID patents are identified below and will be referred to herein as the "MOSAID Patents" or the "patents-in-suit."
- 4. As explained further below, MOSAID has repeatedly asserted that every company that makes DRAM products infringes the MOSAID Patents, and has also repeatedly stated its intention to require every DRAM supplier to take a license to the MOSAID Patents. MOSAID claims to have put virtually every DRAM supplier (large and small), including Micron, on "notice" of its infringement. MOSAID has also sued every leading DRAM supplier for infringing some or all of the MOSAID Patents and, with its June 14, 2006 settlement with Infineon, has now licensed every leading DRAM supplier, except Micron.
- 5. In announcing its settlement with Infineon, MOSAID reaffirmed its commitment to license every remaining unlicensed DRAM supplier and said that it would be "unrelenting" in the assertion of its patents. It also stated that its lawsuits against Samsung, Hynix and Infineon had demonstrated MOSAID's "willingness to use the legal system to enforce [its] patents." In view of these circumstances, as well as MOSAID's statements that the top four DRAM suppliers (Samsung, Hynix, Micron and Infineon) account for over 75% of worldwide DRAM sales, and that the market is "essentially split into very large and quite small DRAM producers," it is clear that MOSAID's

next target will be Micron. Indeed, press reports about the Infineon settlement have noted that the market expects MOSAID to sue Micron for patent infringement.

- 6. MOSAID has also taken concrete steps to prepare to enforce its patents through litigation against Micron by its announcement that a "key aspect" of its recent settlement agreement with Infineon is that Infineon has agreed to file with MOSAID a joint motion to vacate both the claim construction ruling in the *Infineon* case, which was largely unfavorable to MOSAID, and the summary judgment ruling in that case that Infineon did not infringe six of MOSAID's patents. MOSAID has openly stated that its purpose in seeking to have these rulings vacated is to "reset the clock" on those issues, so that "no one can rely on any of those former rulings," and thereby "put [us] in a stronger position for other potential licensees." MOSAID's efforts to vacate these adverse rulings confirm its intent to enforce its patents through litigation.
- 7. Under these circumstances, Micron is under a reasonable apprehension that MOSAID will sue Micron in the near future for alleged infringement of the MOSAID Patents. Micron believes that it does not infringe the MOSAID Patents and that the MOSAID Patents are invalid. Accordingly, there is an actual and justiciable controversy between Micron and MOSAID as to whether Micron infringes the MOSAID Patents and whether the MOSAID Patents are valid. On that basis, Micron brings this declaratory judgment action.

JURISDICTION, VENUE AND INTRADISTRICT ASSIGNMENT

- 8. This Court has jurisdiction over the subject matter of this lawsuit pursuant to 28 U.S.C. §§ 2201-2202, 1331 and 1338(a).
- 9. Venue is proper in this judicial district under 28 U.S.C. §§ 1391(b), (c) and (d), and 1400(b). MOSAID is subject to personal jurisdiction in this district because, among other reasons, it has purposefully directed activities to this district, maintains two offices in this district, and has general and systematic contacts with this district.
- 10. This is an intellectual property action; therefore, pursuant to Civil L.R. 3-2(c), it may be assigned on a district-wide basis. Nevertheless, Micron believes this case is related to another case currently pending in this Court before the Honorable Jeremy Fogel, captioned *Infineon*

Technologies North America Corp. v. MOSAID Technologies Incorporated, Case No. 5:02-cv-05772-JF (RS), and efficiency considerations therefore dictate that it be assigned to Judge Fogel.

BACKGROUND

- 11. Micron is an industry leading, global manufacturer and marketer of semiconductor devices, principally DRAM (dynamic random access memory) devices that provide high-speed data storage and retrieval in personal computers, servers and other products, but also NAND flash memory products and CMOS image sensors. DRAM products comprised 87%, 92% and 96% of Micron's sales in 2005, 2004 and 2003, respectively.
- 12. MOSAID claims to have invented many fundamental DRAM technologies, including circuits that MOSAID claims have been widely adopted by the DRAM industry. MOSAID owns numerous patents that it contends cover these fundamental DRAM technologies and circuits, including the MOSAID Patents at issue in this declaratory judgment action. Starting at least as early as 2000, MOSAID has consistently and repeatedly stated that it believes that "all companies that manufacture state-of-the-art DRAM products . . . use MOSAID's patented circuit technology." Indeed, in its 2000 Annual Report, MOSAID stated: "We patented those [memory] innovations, and now, years later, anybody who wants to design practical, state-of-the-art DRAM has to use our technology."
- 13. In or around 2000, MOSAID launched what it described as an effort to "[seek] the full value of [its patents] by pursuing patent licensing agreements with the world's DRAM manufacturers." MOSAID has consistently and repeatedly stated that its objective is to license its patents to "all" DRAM-makers. Indeed, MOSAID recently reaffirmed that licensing the entire DRAM industry is one of the company's key strategic objectives and that it is "committed" to achieving this objective, will move "aggressively" to do so and will be "unrelenting in the assertion of [its] patent portfolio."
- 14. Today, and for the past several years, there have been four leading competitors in the DRAM industry: Samsung, Hynix, Infineon and Micron. According to MOSAID and industry reports, these four companies today account for 75-80% of worldwide DRAM sales.

- 15. In a series of letters to Micron in 2001 and 2002, MOSAID asserted that several of Micron's DRAM products infringed several MOSAID DRAM patents. Upon information and belief, MOSAID sent similar letters to other DRAM manufacturers, including Samsung, Hynix and Infineon, in that same timeframe. In those letters, MOSAID touted license agreements it had entered into with several other companies, none of which was a major DRAM supplier.
- 16. In September 2001, MOSAID began to use litigation as a means to force the leading DRAM companies to license its DRAM patents. Specifically, MOSAID sued Samsung for patent infringement in the District of New Jersey. MOSAID stated that it sued Samsung "when negotiations failed to progress." Samsung was in 2001, and remains today, the world's largest DRAM manufacturer by revenue.
- MOSAID's complaint against Samsung accused Samsung of infringing seven MOSAID patents. Four of the asserted patents were from what MOSAID refers to as the "Lines Family of Patents": U.S. Patent Nos. 5,214,602, 5,751,643, 5,882,253 and 6,278,640, all of which derive from a common parent application. The other three asserted patents were from what MOSAID refers to as the "Foss Family of Patents": U.S. Patent Nos. 5,828,620, 6,055,201 and 6,236,581, all of which derive from a common parent application.
- 18. In its September 13, 2001, press release announcing the filing of its lawsuit against Samsung, MOSAID stated that the seven patents it had asserted against Samsung were "fundamental Dynamic Random Access Memory (DRAM) circuit inventions, which MOSAID pioneered," and that these circuits "ha[d] been widely adopted by the DRAM industry." MOSAID alleged that Samsung was infringing its patents merely by making and selling DRAM products.
- 19. In December 2002, Infineon, another of the world's top four DRAM makers, filed a declaratory judgment action against MOSAID in this Court. According to Infineon's declaratory judgment complaint, MOSAID had: (i) accused Infineon of infringing numerous MOSAID patents by manufacturing, selling and/or using DRAM products; (ii) demanded that Infineon pay exorbitant sums to MOSAID for its alleged infringement; and (iii) informed Infineon that it would not go away until Infineon licensed MOSAID's patents. Infineon's complaint sought a declaration of non-infringement and invalidity of each of the seven Mosaid patents that MOSAID had asserted against

Samsung the New Jersey litigation. Significantly, MOSAID did not dispute that there was a justiciable controversy between it and Infineon regarding the infringement and validity of the MOSAID patents that were the subject of the declaratory judgment action. Instead, MOSAID filed a counterclaim, asserting that Infineon infringed each of the seven MOSAID patents that were the subject of Infineon's declaratory judgment complaint.

- 20. In June 2003, MOSAID amended its counterclaim against Infineon to assert infringement by Infineon of an additional patent—U.S. Patent No. 6,067,272—covering, according to MOSAID, a "Delay Locked Loop (DLL) circuit for use in Synchronous DRAM." According to MOSAID's June 5, 2003 press release: "We maintain our position that Infineon Technologies is infringing many of MOSAID's patents and this most recent determination of infringement is an important addition to our case. . . . The delay locked loop patent is a valuable component of our intellectual property portfolio. The memory industry is rapidly adopting DLL techniques as high-speed DDR SDRAM memory becomes mainstream." Like the seven patents it had originally asserted against Infineon, MOSAID characterized the '272 patent as a "fundamental" DRAM patent.
- 21. In September 2003, the U.S. Judicial Panel on Multidistrict Litigation consolidated the *Infineon* and the *Samsung* cases in the District of New Jersey (William Martini, J.) for coordinated pretrial proceedings.
- 22. In November 2003, MOSAID amended its complaint against Samsung and its counterclaim against Infineon to assert infringement of two newly-issued patents. These patents were U.S. Patent No. 6,603,703—another patent in the Lines Family of Patents—and U.S. Patent No. 6,580,654—another patent in the Foss Family of Patents. In a November 3, 2003 press release announcing the addition of these patents to the cases, MOSAID stated: "The U.S. Patent Office granted these new patents after considering allegations of prior invention documented by Samsung during the current litigation. . . . We therefore believe the addition of these two new patents will strengthen our ongoing cases against both Samsung and Infineon. . . ." As with the patents it had previously asserted against Infineon and Samsung, MOSAID characterized these newly-added patents as "fundamental" DRAM patents.

- 23. MOSAID announced the settlement of its litigation with Samsung on January 18, 2005. In its press release announcing the settlement, MOSAID restated its intention to force all DRAM suppliers to license its patents and its willingness to use litigation to do so.
- 24. True to its words, on January 18, 2005—the same day it announced its settlement with Samsung—MOSAID sued Hynix, another of the top four DRAM manufacturers, in the Eastern District of Texas. MOSAID accused Hynix of infringing six patents: three Lines Family patents (U.S. Patent Nos. 5,822,253, 6,278,640 and 6,603,703), and three Foss Family patents (5,828,620, 6,055, 201 and 6,580,654). (Each of those patents had been asserted in the cases against Samsung and Infineon.) MOSAID's press release announcing the suit against Hynix stated that the patents it had asserted against Hynix were "fundamental [DRAM] circuit inventions" and that Hynix was infringing them "by making and selling products, including DRAMs."
- 25. MOSAID announced a settlement with Hynix less than a month later, on February 17, 2005. In its press release announcing its settlement with Hynix, MOSAID once again stated its unwavering intention to force every DRAM supplier to license its patents and its willingness to use litigation in achieving that goal.
- 26. On April 4, 2005, Judge Martini of the District of New Jersey granted Infineon's motion for summary judgment that Infineon's accused DRAM products did not infringe any of the asserted claims of three of the Lines patents (Nos. 5,822,253, 5,751,643 and 6,278,640) and three of the Foss patents (Nos. 5,828,620, 6,055,201 and 6,580,654). The court did not make any ruling as to whether Infineon's products infringed MOSAID Patent No. 6,603,703. Those summary judgment rulings were based in part on claim construction rulings the court had issued in March 2004. These claim construction rulings were largely unfavorable to MOSAID.
- 27. Just three days later, in response to the adverse summary judgment ruling on six of its patents in the New Jersey action, MOSAID filed a new patent infringement suit against Infineon in the Eastern District of Texas. This new suit accused Infineon of infringing three additional MOSAID patents that purportedly cover DRAM products. These patents were U.S. Patent Nos. 6,657,919, 6,057,676 and RE 37,641. In its press release announcing the new Texas suit, MOSAID stated that Infineon "has infringed, and is infringing, by making and selling products, including

DRAMs," that these "are patents from three patent families which MOSAID refers to as delay locked loop, cell plate voltage and bit-line isolation families," that "[t]he three patent families in this Texas litigation are different than the two patent families at issue in the New Jersey litigation with Infineon," and that "[t]he fact that MOSAID is able to bring litigation on multiple patent families also demonstrates why twelve major companies have taken comprehensive licenses to MOSAID's patent portfolio." In January 2006, MOSAID added to the Eastern District of Texas case another "delay locked loop" patent—U.S. Patent No. 6,992,950. MOSAID added this patent to the case on the day that patent issued.

- 28. In or about August 2005, with the New Jersey court having concluded its pretrial activities, the MDL Panel transferred the *Infineon v. MOSAID* case back to the Northern District of California. In October 2005, Judge Fogel issued a Stipulation and Order Granting MOSAID's Motion for Entry of Final Judgment Pursuant to Rule 54(b) and Staying Remaining Issues in Case. In the stipulation, MOSAID and Infineon agreed that MOSAID could immediately appeal Judge Martini's summary judgment and claim construction orders (collectively, "the *Infineon* Orders") and postpone litigating the remaining claims before Judge Fogel until the appeal on these issues was completed.
- 29. In November 2005, MOSAID filed a Notice of Appeal with respect to the judgment in the *Infineon* case.
- 30. On June 14, 2006, before any briefs had been filed in connection with the appeal, MOSAID and Infineon announced that they had reached a settlement of all outstanding litigation between the companies. In a web-cast conference call regarding the settlement, MOSAID's executives stated that: MOSAID "ha[s] now signed licenses with the majority of the world's large and mid-sized DRAM manufacturers"; that thirteen companies were on "notice" with respect to MOSAID's patents; that MOSAID would be "unrelenting in the assertion of [its] patent portfolio"; and that MOSAID had demonstrated a "willingness to use the legal system to enforce [its] patents."
- 31. After MOSAID's settlement with Infineon, Micron is the only remaining major DRAM manufacturer in the world that has not been sued by MOSAID and has not agreed to license MOSAID's patent portfolio. Given MOSAID's long-stated objective to license all DRAM-makers

and the fact that MOSAID sued each of Micron's major competitors when they refused to agree to MOSAID's licensing demands, it is a virtual certainty that MOSAID will sue Micron for infringement of at least the patents that it asserted in its suits against Samsung, Infineon and Hynix. Indeed, MOSAID's statement in its 2005 Annual Report that the "the top four producers" of DRAM "now represent[] 76% of the industry . . . results in a licensing market that is essentially split into very large and quite small DRAM producers," is an admission by MOSAID that its focus will be on the "very large" DRAM suppliers, of which only Micron remained unlicensed.

- 32. MOSAID has made numerous public statements, including in press releases and in its annual reports, that confirm its intent to sue Micron now that the *Infineon* litigation has settled. For example, according to MOSAID's 2005 Annual Report, MOSAID's primary strategic objective is to "License balance of DRAM market," and that "[w]ith approximately half the DRAM industry now under license, it is clear that our first task is to license the remaining DRAM manufacturers." MOSAID's 2005 Annual Report also reiterated MOSAID's belief that "all companies that manufacture state-of-the-art DRAM products . . . use MOSAID's patented circuit technology," and stated that its IP Division plans to "aggressively seek licensing revenues from all companies using MOSAID's DRAM technology."
- 33. MOSAID's intent to bring a patent infringement suit against Micron is further evidenced by the fact that MOSAID has touted as "key aspect" of its settlement with Infineon that "MOSAID and Infineon have also agreed to file a joint motion requesting the Court in California to vacate the Summary Judgment Order issued" in April 2005. MOSAID's insistence on Infineon's consent to the parties' moving to vacate Judge Martini's adverse rulings is clear evidence that MOSAID once again intends to assert the MOSAID Patents in litigation. Indeed, in its June 14, 2006 conference call about the settlement with Infineon, MOSAID's executives openly acknowledged that MOSAID's purpose in having the *Infineon* Orders vacated is to "reset the clock" on those issues, so that "no one can rely on any of those former rulings," and thereby "put [us] in a stronger position for other potential licensees."
- 34. MOSAID also stated in the June 14 conference call that it does not expect its litigation expenses to drop despite the Infineon settlement, because "litigation is always a

possibility for us." Further, in response to a question during the conference call, MOSAID's executives did not deny the "logic" of litigation with "the other major DRAM manufacturer you [MOSAID] haven't signed," clearly meaning Micron. MOSAID further stated that it expected that other accused infringers "are paying attention" to the Infineon settlement.

- 35. In the wake of MOSAID's settlement with Infineon, press reports have predicted that MOSAID will sue Micron. For example, a Reuters story of June 14, 2006 stated that "[i]t is expected that Mosaid will next go after Micron Technology now that it has settled with DRAM memory chip leaders Infineon, Samsung Electronics and Hynix." [Reuters, Mosaid settles Infineon patent row, ups forecasts, June 14, 2006, available at http://yahoo.reuters.com/news] Another article stated that now that "MOSAID has secured licensing agreements with three of the world's top four manufacturers of DRAM technology that collectively account for 75 percent of the market" and that "the market expects MOSAID will soon launch action against the fourth—Micron Technologies." [Ottawa Bus. J., June 21, 2006, http://www.ottawabusinessjournal.com (search "Archive Search" for "MOSAID deal falls short") (emphasis added)]
- 36. Micron believes that the only reason MOSAID has not yet sued it for patent infringement is that MOSAID wants to attempt to have the adverse *Infineon* Orders vacated before it sues Micron. Indeed, at a June 9, 2006 status conference before this Court in the *Infineon* case, in which MOSAID's counsel advised this Court of its plan to file a motion to vacate, the Court asked MOSAID's counsel whether there were any other pending cases involving the MOSAID patents at issue in that case. MOSAID's counsel's telling response was: "Not at this time. There's nothing pending."
- 37. In view of MOSAID's position that all DRAM products infringe the MOSAID Patents, its stated intention to force all DRAM suppliers to license its patents, its history of suing companies that refuse to take a license, its recently concluded license with the last major DRAM supplier other than Micron, its stated primary objective to "license [the] balance of [the] DRAM market," and its efforts to have vacated the adverse rulings in the Infineon case, Micron is under a reasonable apprehension of suit by MOSAID with respect to the MOSAID Patents.

4 5

6 7

9

8

11 12

10

13

14

15

16

17 18

19

20 21

22

23 24

25

27

26

28

38. Contrary to MOSAID's accusations, Micron believes that it is not infringing the MOSAID Patents and that the MOSAID Patents are invalid.

THE MOSAID PATENTS-IN-SUIT

- 39. Micron incorporates and realleges the allegations of paragraphs 1-38 as if set forth fully herein.
- 40. Based on MOSAID's conduct and statements, Micron seeks a declaratory judgment of patent non-infringement and invalidity of the following MOSAID Patents:

"Lines Family Patents"

- U.S. Patent No. 5,214,602 ("the '602 patent") entitled "Dynamic Memory Word Line Driver Scheme," which was filed on April 5, 1991 and issued on May 25, 1993. The inventor named on the '602 patent is Valerie L. Lines. A copy of the '602 patent is attached hereto as Exhibit A.
- U.S. Patent No. 5,751,643 ("the '643 patent") entitled "Dynamic Memory Word Line Driver," which was filed on March 6, 1996 and issued on May 12, 1998. The inventor named on the '643 patent is Valerie L. Lines. A copy of the '643 patent is attached hereto as Exhibit B.
- U.S. Patent No. 5,822,253 ("the '253 patent") entitled "Dynamic Memory Word Line Driver Scheme," which was filed on August 16, 1995 and issued on October 13, 1998. The inventor named on the '253 patent is Valerie L. Lines. A copy of the '253 patent is attached hereto as Exhibit C.
- U.S. Patent No. 6,278,640 ("the '640 patent") entitled "Dynamic Memory Word Line Driver Scheme," which was filed on April 13, 2000 and issued on August 21, 2001. The inventor named on the '640 patent is Valerie L. Lines. A copy of the '640 patent is attached hereto as Exhibit D.
- U.S. Patent No. 6,603,703 B2 ("the '703 patent"), entitled "Dynamic Memory Word Line Driver Scheme," which was filed on July 31, 2001 and issued on August 5, 2003. The inventor named on the '703 patent is Valerie L. Lines. A copy of the '703 patent is attached hereto as Exhibit E.

"Foss Family Patents"

- U.S. Patent 5,828,620 ("the '620 patent") entitled "High Voltage Boosted Word Line Supply Charge Pump and Regulator for DRAM," which was filed on September 2, 1997 and issued on October 27, 1998. The inventors named on the '620 patent are Richard C. Foss, Peter B. Gillingham, Robert F. Harland and Valerie L. Lines. A copy of the '620 patent is attached hereto as Exhibit F.
- U.S. Patent No. 6,055,201 ("the '201 patent") entitled "High Voltage Boosted Word Line Supply Charge Pump and Regulator for DRAM," which was filed on October 26, 1998 and issued on April 25, 2000. The inventors named on the '201 patent are Richard C. Foss, Peter B. Gillingham, Robert F. Harland and Valerie L. Lines. A copy of the '201 patent is attached hereto as Exhibit G.
- U.S. Patent No. 6,236,581 B1 ("the '581 patent") entitled "High Voltage Boosted Word Line Supply Charge Pump and Regulator for DRAM," which was filed on January 14, 2000 and issued on May 22, 2001. The inventors named in the '581 patent are Richard C. Foss, Peter B. Gillingham, Robert F. Harland and Valerie L. Lines. A copy of the '581 patent is attached hereto as Exhibit H.
- U.S. Patent No. 6,580,654 B2 ("the '654 patent"), entitled "Boosted Voltage Supply," which was filed on January 24, 2002 and issued on June 17, 2003. The inventors named on the '654 patent are Richard C. Foss, Peter B. Gillingham, Robert F. Harland and Valerie L. Lines. A copy of the '654 patent is attached hereto as Exhibit I.

"Cell Plate Family Patents"

• U.S. Patent No. 6,057,676 ("the '676 patent"), entitled "Regulated DRAM Cell Plate and Precharge Voltage Generator," which was filed on June 18, 1998 and issued on May 2, 2000. The inventors named on the '676 patent are Ki-Jun Lee, Gurpreet Bhullar and Michael B Vladescu. A copy of the '676 patent is attached hereto as Exhibit J.

"Delayed Locked Loop Family Patents"

- U.S. Patent No. 6,067,272 ("the '272 patent"), entitled "Delayed Locked Loop Implementation in a Synchronous Dynamic Random Access Memory," which was filed on December 22, 1997 and issued on May 23, 2000. The inventors named on the '272 patent are Richard C. Foss, Peter B. Gillingham and Graham Allan. A copy of the '272 patent is attached hereto as Exhibit K.
- U.S. Patent No. 6,657,919 B2 ("the '919 patent"), entitled "Delayed Locked Loop Implementation in a Synchronous Dynamic Random Access Memory," which was filed on January 17, 2003 and issued on December 2, 2003. The inventors named on the '919 patent are Richard C. Foss, Peter B. Gillingham and Graham Allan. A copy of the '919 patent is attached hereto as Exhibit L.
- U.S. Patent No. 6,992,950 B2 ("the '950 patent"), entitled "Delayed Locked Loop Implementation in a Synchronous Dynamic Random Access Memory," which was filed on August 21, 2003 and issued on January 31, 2006. The inventors named on the '950 patent are Richard C. Foss, Peter B. Gillingham and Graham Allan. A copy of the '950 patent is attached hereto as Exhibit M.

"Bit-Line Isolation Family Patents"

- Reissued U.S. Patent No. 37,641 ("the '641 patent"), entitled "Dynamic Random Access Memory Using Imperfect Isolating Transistors," which was filed on May 8, 1997 and issued on April 9, 2002. The inventors named on the '641 patent are Richard C. Foss, Peter B. Gillingham, Robert Harland, Masami Mitsuhashi and Atsushi Wada. A copy of the '641 patent is attached hereto as Exhibit N.
- 41. On information and belief, MOSAID is the owner by assignment of each of the patents-in-suit.

COUNT I

DECLARATORY JUDGMENT

42. Micron incorporates and realleges the allegations of paragraphs 1-41 as if set forth fully herein.

- 43. A valid and justiciable controversy has arisen and exists between MOSAID and Micron regarding the patents-in-suit. Micron desires a judicial determination and a declaration of the respective rights of the parties regarding the patents-in-suit.
 - 44. Each claim of the patents-in-suit is invalid.
 - 45. Micron has not directly or indirectly infringed any claim of the patents-in-suit.

REQUEST FOR RELIEF

WHEREFORE, Micron requests that this Court:

- a. declare that U.S. Patent Nos. 5,214,602; 5,751,643; 5,822,253; 6,278,640; 6,603,703 B2; 5,828,620; 6,055,201; 6,236,581 B1; 6,580,654 B2; 6,057,676; 6,067,272; 6,657,919 B2; 6,992,950 B2; Reissued U.S. Patent No. 37,641 are invalid;
- b. declare that Micron has not and is not directly or indirectly infringing U.S. Patent Nos. 5,214,602; 5,751,643; 5,822,253; 6,278,640; 6,603,703 B2; 5,828,620; 6,055,201; 6,236,581 B1; 6,580,654 B2; 6,057,676; 6,067,272; 6,657,919 B2; 6,992,950 B2; Reissued U.S. Patent No. 37,641;
- c. declare this to be an exceptional case under 35 U.S.C. § 285 and award Micron its attorneys' fees, costs and expenses in connection with this action; and
 - d. award Micron such other and further relief as to which it may be entitled.

PERKINS COIE BROWN & BAIN P.A. PERKINS COIE LLP

By

Jonathan M. James Christopher M. Schultz

David M. LaSpaluto

PERKINS COIE BROWN & BAIN P.A.

2901 North Central Avenue

Post Office Box 400

Phoenix, Arizona 85001-0400

Stefani E. Shanberg PERKINS COIE LLP Four Embarcadero Center Suite 2400 San Francisco, California 94111

Attorneys for MICRON TECHNOLOGY, INC.

1 **DEMAND FOR JURY TRIAL** Plaintiff Micron Technology, Inc. hereby demands a trial by jury of this action. 2 3 July 24, 2006. 4 PERKINS COIE BROWN & BAIN P.A. PERKINS COIE LLP 5 6 7 Jonathan M. James Christopher M. Schultz 8 David M. LaSpaluto PERKINS COIE BROWN & BAIN P.A. 9 2901 North Central Avenue Post Office Box 400 10 Phoenix, Arizona 85001-0400 11 Stefani E. Shanberg PERKINS COIE LLP 12 Four Embarcadero Center **Suite 2400** 13 San Francisco, California 94111 14 Attorneys for MICRON TECHNOLOGY, INC. 15 16 17 18 19 20 21 22 23 24 25 26 27 28

CERTIFICATION OF INTERESTED ENTITIES OR PERSONS 1 2 Pursuant to Civil Local Rule 3-16, the undersigned certifies that as of this date, other than the 3 named parties, there is no such interest to report. 4 July 24, 2006. PERKINS COIE BROWN & BAIN P.A. 5 PERKINS COIE LLP 6 7 8 Jonathan M. James Christopher M. Schultz 9 David M. LaSpaluto PERKINS COIE BROWN & BAIN P.A. 10 2901 North Central Avenue Post Office Box 400 11 Phoenix, Arizona 85001-0400 12 Stefani E. Shanberg PERKINS COIE LLP 13 Four Embarcadero Center **Suite 2400** 14 San Francisco, California 94111 15 Attorneys for MICRON TECHNOLOGY, INC. 16 339815_3 17 18 19 20 21 22 23 24 25 26 27 28